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54 Process for forming passivation film on photoelectric conversion device and the device produced thereby.

57 In a process for forming passivation film on the surface of a photoelectric conversion device having junction between hydrogenated amorphous silicon and a conductive electrode, the passivation film is formed by steps of forming a first passivation film of silicon oxide on the surface of the photoelectric conversion device and then forming a second passivation film of silicon nitride on the first passivation film. In this process, the first passivation film is formed under atmosphere of a mixed gas prepared by mixing excessively a gas containing oxygen with silane gas in accordance with the plasma CVD method.

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PROCESS FOR FORMING PASSIVATION FILM ON PHOTOELECTRIC
CONVERSION DEVICE AND THE DEVICE PRODUCED THEREBY

BACKGROUND OF THE INVENTION

5 1. Field of the Invention

This invention relates to a photoelectric conversion device, and particularly to a process for forming passivation film on a photoelectric conversion device wherein hydrogenated amorphous silicon is employed as
10 a photoconductor, and to the photoelectric conversion device produced by said process.

2. Description of the Prior Art

Of photoelectric conversion devices converting optical signal into electrical signal, there is an image sensor
15 for reading an original in a facsimile equipment and the like.

Figs. 1(a) and 1(b) are sectional views showing conventional planar type image sensors. The image sensor 10 of Fig. 1(a) has such a construction that opaque
20 electrodes 12 and 13 being opposed to each other are disposed on a substrate 11 with a suitable spacing, and a photoconductive material 14 and a passivation film 15 are successively laminated on the electrodes 12 and 13. Light is applied to the image sensor 10 from the side of
25 the substrate 11 as indicated by an arrow La.

On the other hand, an image sensor 20 shown in Fig. 1(b) has such a construction that a photoconductor 22

is formed on a substrate 21, opaque electrodes 23 and 24 being opposed to each other are disposed on the photoconductor 22 with a suitable spacing, and these electrodes 23 and 24 are further covered with a passivation film 25. Light is applied to the image sensor 20 from the side of the passivation film 25 (as indicated by an arrow Lb) contrary to the case of the image sensor 10 in Fig. 1(a).

Figs. 1(c) and 1(d) are sectional views showing conventional sandwich type image sensors. The image sensor 30 of Fig. 1(c) has such a construction that a transparent lower electrode 32 is formed on a substrate 31, a photoconductor 33 and an opaque upper electrode 34 are successively formed on the lower electrode 32, and a passivation film 35 is applied thereon. Light is applied to the image sensor 30 from the side of the substrate 31 as indicated by an arrow Lc in Fig. 1(c).

An image sensor 40 shown in Fig. 1(d) has such a construction that an opaque lower electrode 42 is formed on a substrate 41, a photoconductor 43 and a transparent upper electrode 44 are successively formed on these substrate 41 and lower electrode 42, and a passivation film 45 is further applied thereon. In this case, light is applied to the image sensor 40 from the side of the passivation film 45 as indicated by an arrow Ld.

The substrate 11 in the image sensor 10 to which light is applied is transparent so that the

substrate 11 functions also as the passivation film. Since the reverse passivation film 15 may be opaque, material and film thickness of the passivation film 15 can be selected comparatively tolerantly.

5 The image sensor 30 of sandwich construction is also in the same situation as that of the image sensor 10.

On the other hand, the passivation films 25 and 45 must be transparent in the image sensors 20 and 40 because light is applied from the sides of the passivation
10 films.

Material for such passivation films may be phosphorous glass (PSG) prepared by CVD (Chemical Vapor Deposition) method, and silicon nitride or silicon oxide prepared by plasma CVD method, besides polyimide
15 organic film and the like. Of the above materials, the most preferable is silicon nitride which is hardly permeable to water or an alkali ion such as Na and also, heat resistive.

However, both silicon nitride utilized for the
20 passivation film and amorphous silicon employed for the photoconductor have high residual stress, so that the silicon nitride film is likely to peel from the amorphous silicon film at the contacting portion therebetween. Such peeling becomes a cause for deterioration in
25 current-voltage characteristics of photoelectric conversion device.

When a hydrogenated amorphous silicon film is heated

at a temperature higher than 400°C, elimination reaction of hydrogen from silicon atom arises. For this reason, it is preferable that the hydrogenated amorphous silicon film is not heated at a temperature higher than 400°C in the process forming the passivation film. Accordingly, the most preferable is plasma CVD method in which low-temperature process treatment can be effected. The plasma CVD method, however, when it is intended for forming directly a nitride silicon film on a photoelectric conversion element, has such a disadvantage that an ITO (indium-tin oxide) film changes in quality in early stage of forming the film, and characteristics of the junction between amorphous silicon and ITO film deteriorate so that dark current increases greatly.

15 SUMMARY OF THE INVENTION

Accordingly, it is an object of the present invention to provide a process for forming passivation film onto a photoelectric conversion device which does not deteriorate photoelectric conversion characteristics even if a silicon nitride film is employed as passivation film as well as the photoelectric conversion device produced by said process.

According to the present invention, a silicon nitride film is not directly formed on a photoelectric conversion device, but a silicon oxide film is first formed on the photoelectric conversion device and then, the silicon nitride film is formed thereon.

Incidentally, when a silicon oxide film is formed on a photoelectric conversion device according to plasma CVD method, since most of reactive gases have a dissociation energy smaller than ionization energy, rate of formation of ion is slow among active seeds (radicals) causing positive ion to recombine with electron, and therefore many neutral radicals (hydrogen radicals) are generated in plasma. Since active seeds other than ion have long life, hydrogen radical contained in silane gas reacts with ITO at the early stage of growth of the silicon oxide film in case of the film formation according to plasma CVD method causing the change in the transparent upper electrode (ITO film). As a result contacting portion between transparent electrode and hydrogenated amorphous silicon deteriorates, causing significant decrease in light/dark current ratio due to the increase of dark current.

Accordingly, in the present invention, a reactive gas containing a large amount of oxygen is mixed with a small amount of silane gas, and a silicon oxide film is first formed on the surface of a photoelectric conversion device in the resulting mixed gas atmosphere in accordance with plasma CVD method.

The present invention provides a process for forming passivation film on the surface of a photoelectric conversion device having junction between hydrogenated amorphous silicon and a conductive electrode,

characterized in that said passivation film is formed by steps of forming a first passivation film of silicon oxide on the surface of said photoelectric conversion device and then forming a second passivation film of silicon
5 nitride on said first passivation film. In this process, the first passivation film is formed under atmosphere of a mixed gas prepared by mixing excessively a gas containing oxygen with silane gas in accordance with plasma CVD method.

Furthermore, the present invention provides a
10 photoelectric conversion device having junction between hydrogenated amorphous silicon and a conductive electrode and a passivation film formed on the surface thereof, characterized in that said passivation film has two-layer construction composed of a first passivation film of
15 silicon oxide and a second passivation film of silicon nitride, both films being formed in accordance with plasma CVD method.

BRIEF DESCRIPTION OF THE DRAWINGS

In the accompanying drawings:

20 Figs. 1(a) to 1(d) are partial sectional views showing various constructions of conventional image sensors;

Fig. 2 is a partial sectional view showing a photoelectric conversion device covered with a
25 passivation film according to the present invention; and

Fig. 3 is a graphical representation illustrating current-voltage characteristic curves of the photoelectric

conversion device of Fig. 2.

DESCRIPTION OF THE PREFERRED EMBODIMENT

Fig. 2 is a sectional view showing a photoelectric conversion device 60 covered with a passivation film in accordance with the present invention.

As shown in Fig. 2, the photoelectric conversion device 60 is prepared by the steps of forming a lower electrode 62 on a glass or ceramic substrate 61, laminating successively amorphous silicon as a photoconductor 63 and a transparent upper electrode 64 on the lower electrode 62, and covering the whole surface of the resulting composite material other than connecting terminals for the lower and upper terminals 62 and 64 (not shown) with a silicon nitride film 50. In addition, a silicon oxide film 51 is placed between the silicon nitride film 50 and the photoelectric conversion device 60 as a buffer film 51.

A process for forming passivation film on the photoelectric conversion device according to the present invention will more specifically be described hereinbelow.

First, the lower electrode 62 of a suitable shape and size is provided by forming a chromium (Cr) film with a thickness of about 3000 Å all over the surface of the glass or glazed ceramic substrate 61 in accordance with electron beam vapor deposition method, and further photoetching the resulting composite material

to a desired shape and size. Then, silane (SiH_4) gas is decomposed by means of glow discharge to deposit hydrogenated amorphous silicon on the substrate 61 with a thickness of about $1\text{ }\mu\text{m}$, thereby forming the photoconductor 63. Next, the transparent upper electrode 64 is provided by forming a film of ITO (indium-tin oxide) with a thickness of about $1500\text{ }\text{\AA}$ in accordance with sputtering process.

Further a passivation film is provided on the photoelectric conversion device prepared in accordance with the process as mentioned above. The passivation film comprises the silicon oxide film 51 and the silicon nitride film 50 and was successively produced experimentally under the following conditions in accordance with plasma CVD method.

The silicon oxide film 51 was formed with a thickness of $3000\text{ }\text{\AA}$ to $2.5\text{ }\mu\text{m}$ for about 1 hour under a substrate temperature of 250°C , vacuum degree of 0.1 to 1.5 Torr, discharge output of 50 to 250 W, silane gas flow rate of 3 to 20 SCCM (Standard cc minute), and dinitrogen monoxide flow rate of 100 to 500 SCCM. Then, the silicon nitride film 50 was formed with a thickness of $7000\text{ }\text{\AA}$ to $9000\text{ }\text{\AA}$ for about 20 minutes under substrate temperature of 250°C , vacuum degree of 0.5 to 1.5 Torr, discharge output of 100 to 200 W, silane gas flow rate of 20 SCCM, ammonia flow rate of 60 SCCM, and nitrogen flow rate of 200 SCCM.

The photoelectric conversion device prepared as described above had very favorable properties with a photoelectric current of 10^{-7} A/cm²·lux and a light/dark current ratio of about 7000. Furthermore, the photoelectric conversion device was subjected to pressure Cook test for 1 hour at 120°C and 2 atmospheric pressure. As a result no substantial changes were observed in its properties.

Now, the current-voltage characteristics of the photoelectric conversion device 60 with the silicon oxide film 51 as described above and covered with the silicon nitride film 50 will be described. In Fig. 3, curves a and a' represent light current and dark current when the photoelectric conversion device is covered with no passivation film (case I), curves b and b' represent light current and dark current when the photoelectric conversion device is covered with the passivation film according to the present invention (case II), curves c and c' represent light current and dark current when the photoelectric conversion device is only covered with the silicon oxide film 51 as a buffer film (case III) and curves d and d' represent light current and dark current when the photoelectric conversion device is only covered with the silicon nitride film 50 (case IV). As seen in Fig. 3, the light current has substantially the same value of 10^{-5} A/cm² in all cases to IV, whilst the dark current increases in case III and IV as compared with case I,

and particularly in case IV it increases by substantially two places. However, it is found that case II, where the photoelectric conversion element was covered with the passivation film is substantially same with case I
5 where the photoelectric conversion device was covered with no passivation film.

Although silicon oxide film has been utilized for a buffer film in the present embodiment, the buffer film is not limited to such silicon oxide film, but any
10 material may be employed as long as it will not deteriorate properties of the boundary between the photoconductor and the transparent upper electrode in forming the buffer film, and as long as it will not be deteriorated its properties during the time when the silicon nitride film
15 is formed.

Further, although the passivation film has been described in connection with the above embodiment wherein photoelectric conversion device is of a sandwich construction and light is applied from the opposite
20 side of the substrate, this invention is of course not limited to the above case, but the passivation film may also be applied to photoelectric conversion device of a planar construction.

The passivation film formed according to the
25 present invention is also suited for use in other photoelectric conversion device such as solar cell and the like.

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In addition, such buffer film and silicon nitride film can function as reflection preventive films by optimizing thickness of each film, so that refractive indices of the buffer film and silicon nitride film
5 become suitable values.

WHAT IS CLAIMED IS:

1. A process for forming passivation film on a photoelectric conversion device having junction between hydrogenated amorphous silicon and electrically conductive electrodes, comprising steps of forming a first passivation film of silicon oxide on the surface of said photoelectric conversion device, and then forming a second passivation film of silicon nitride on said first passivation film.
2. A process for forming passivation film on a photoelectric conversion device as claimed in claim 1, wherein said first passivation film is formed under atmosphere of a mixed gas prepared by mixing excessively a gas containing oxygen with silane gas in accordance with plasma CVD method.
3. A process for forming passivation film on a photoelectric conversion device as claimed in claim 2, wherein said gas containing oxygen is selected from the group consisting of nitrous oxide gas, carbon dioxide gas and oxygen gas.
4. A process for forming passivation film on a photoelectric conversion device as claimed in claim 1, wherein said second passivation film is formed under atmosphere of a mixed gas prepared by mixing ammonia with silane gas in accordance with the plasma CVD method.
5. A process for forming passivation film on a

photoelectric conversion device as claimed in claim 1,
wherein said conductive electrode is composed of
indium-tin oxide.

6. A photoelectric conversion device having junction
between hydrogenated amorphous silicon and electrically
conductive electrodes. and covered with passivation film,
wherein said passivation film is of a two-layer construction
composed of a first passivation film of silicon oxide
formed in accordance with the plasma CVD method and a
second passivation film of silicon nitride formed in
accordance with the plasma CVD method.

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FIG.1(a)
(PRIOR ART)

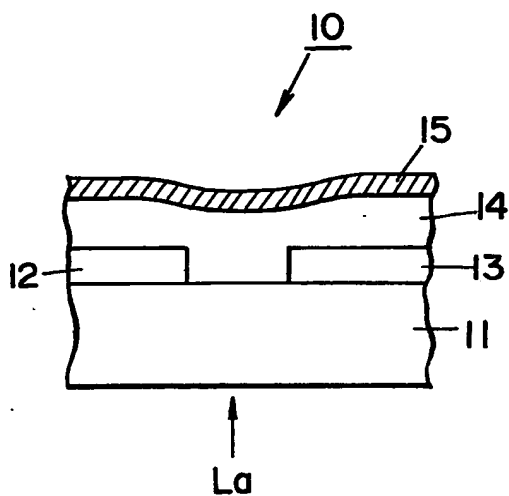


FIG.1(b)
(PRIOR ART)

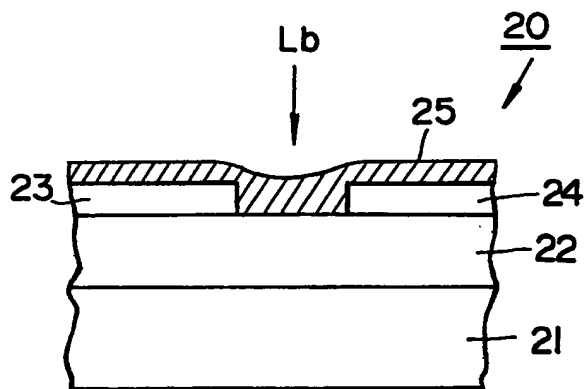


FIG.1(c)
(PRIOR ART)

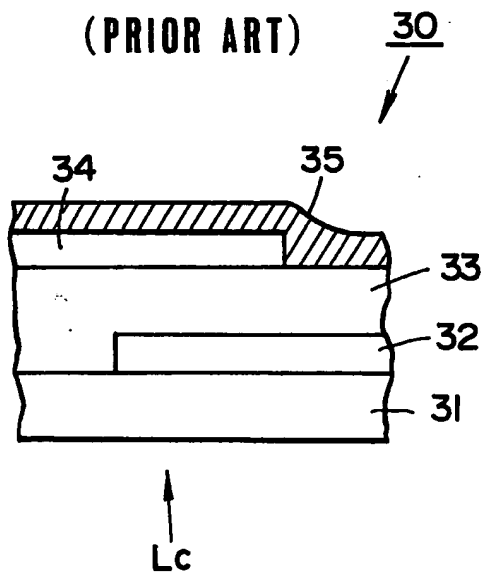
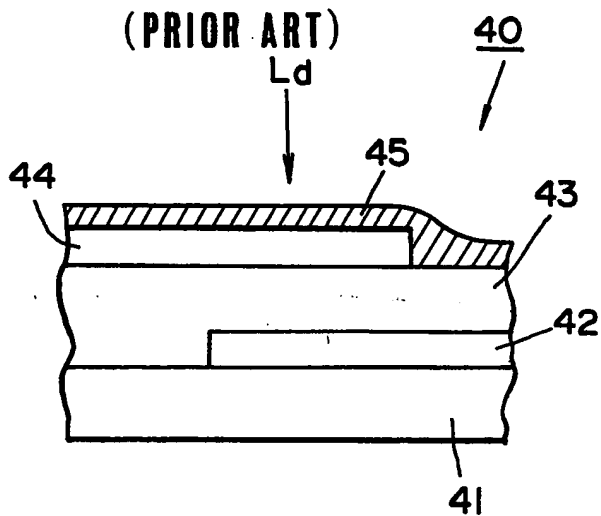
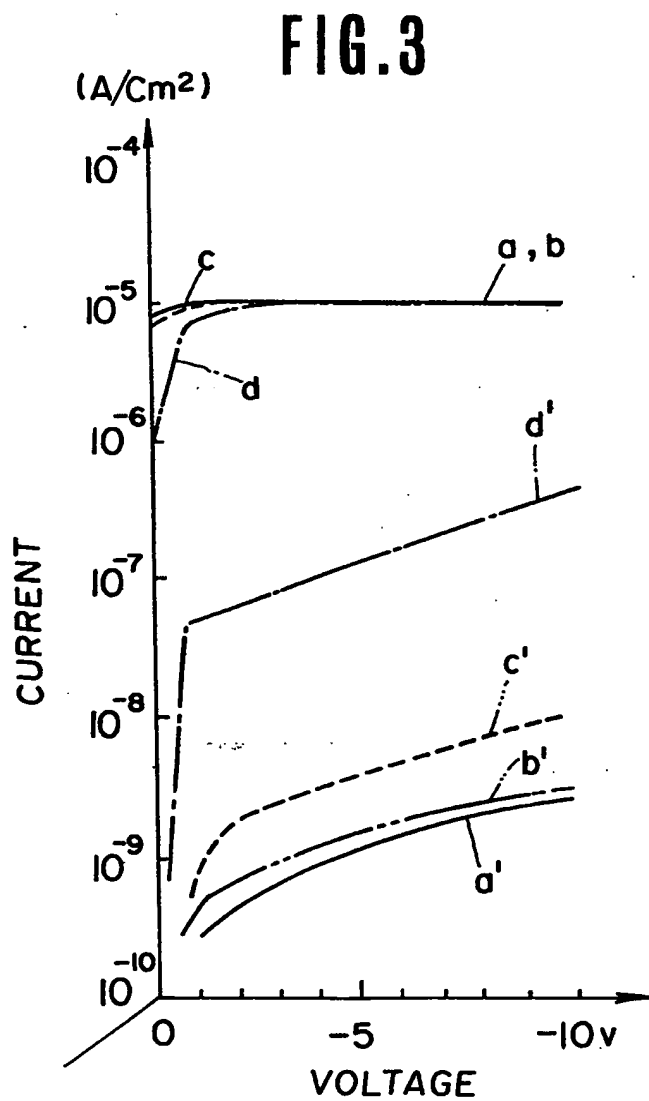
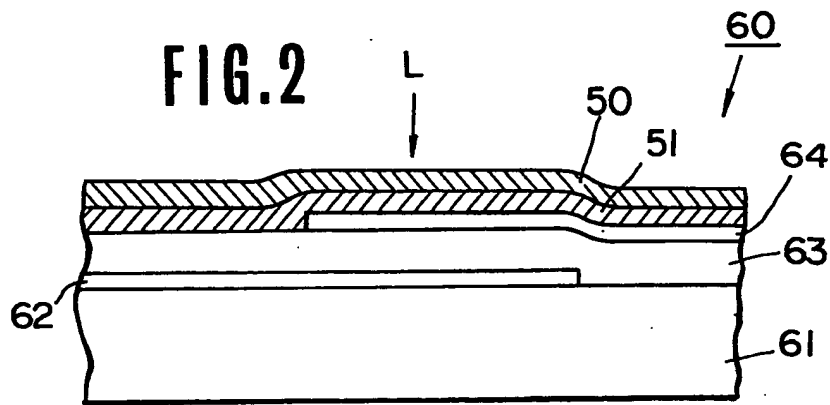


FIG.1(d)
(PRIOR ART)





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